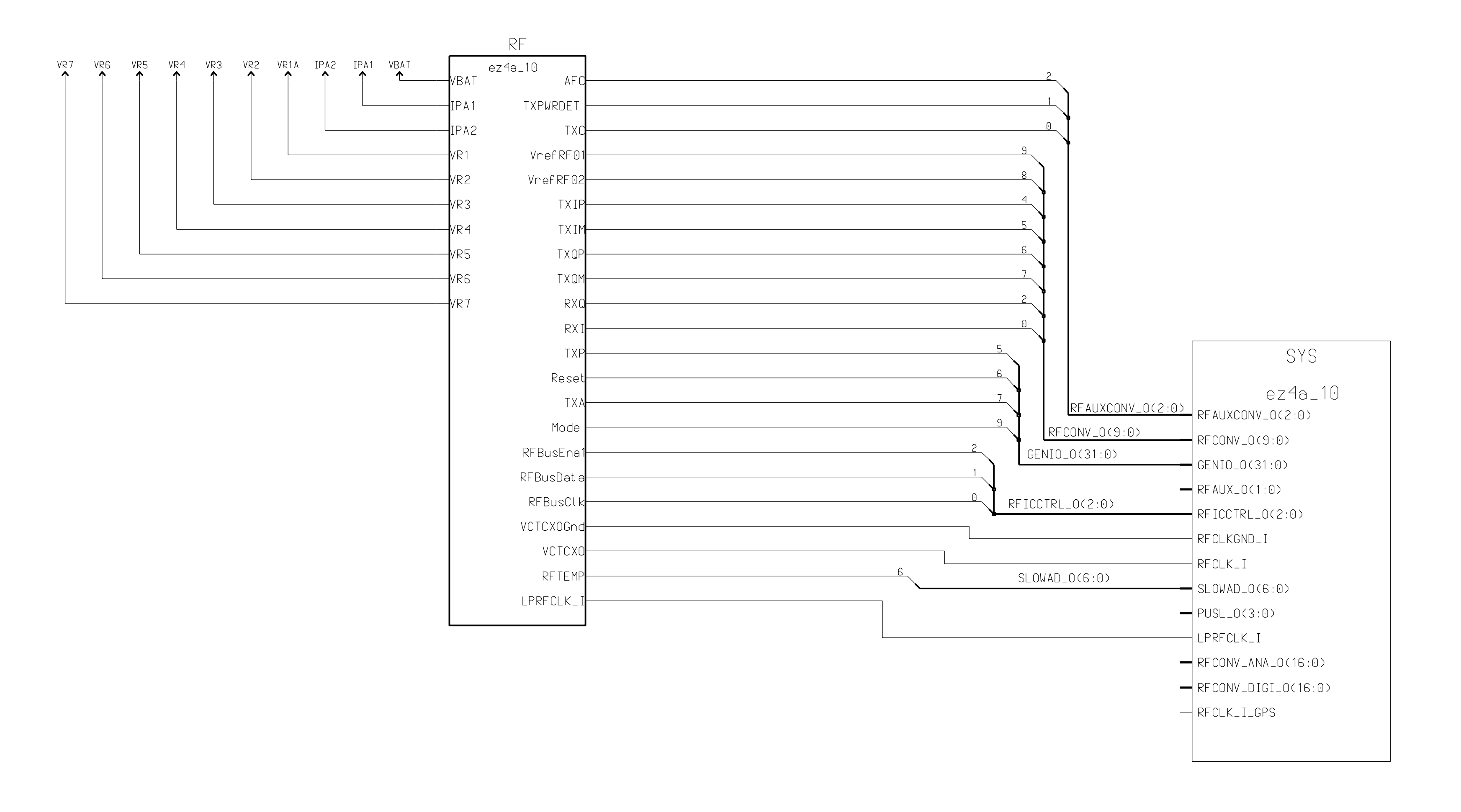
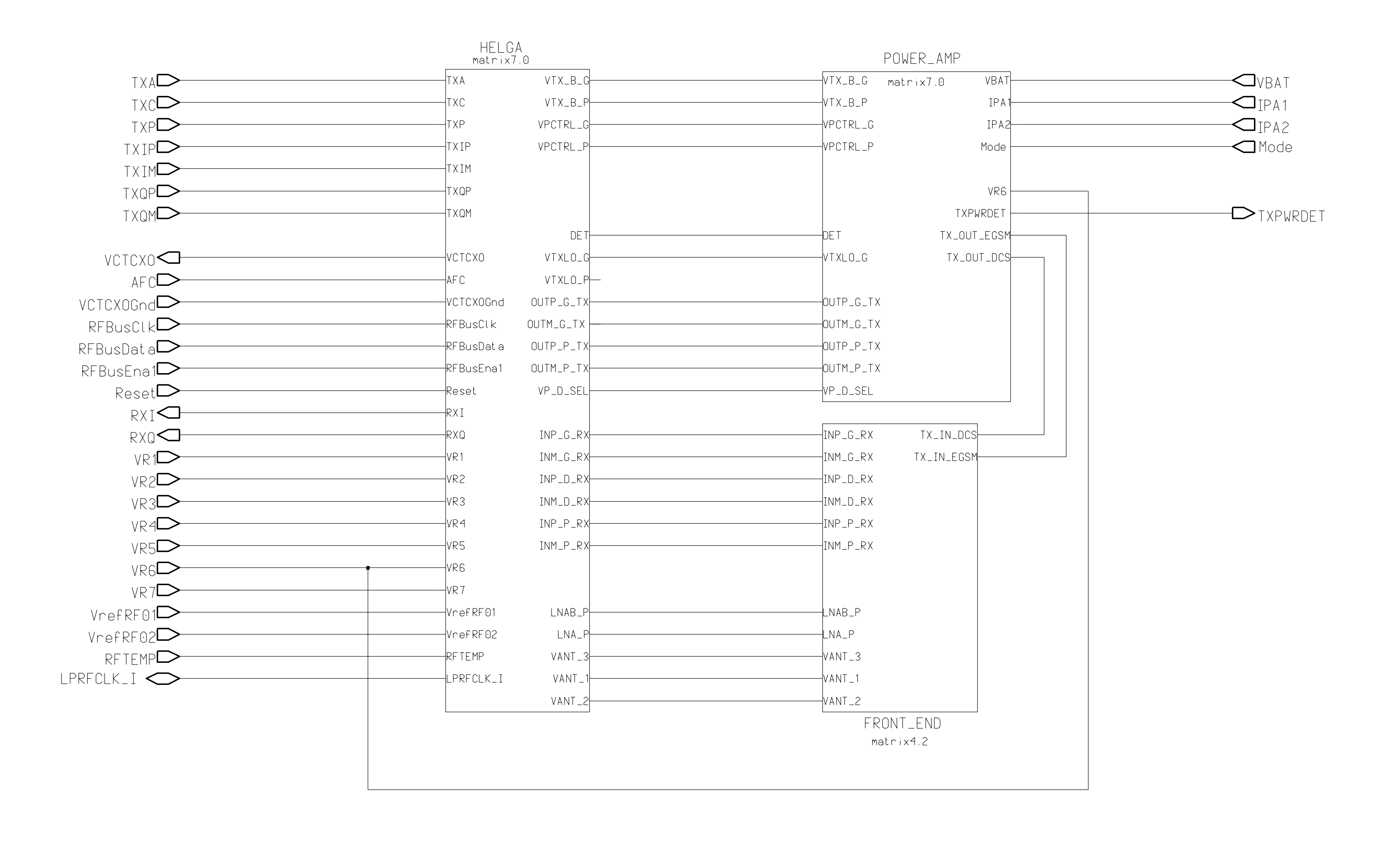
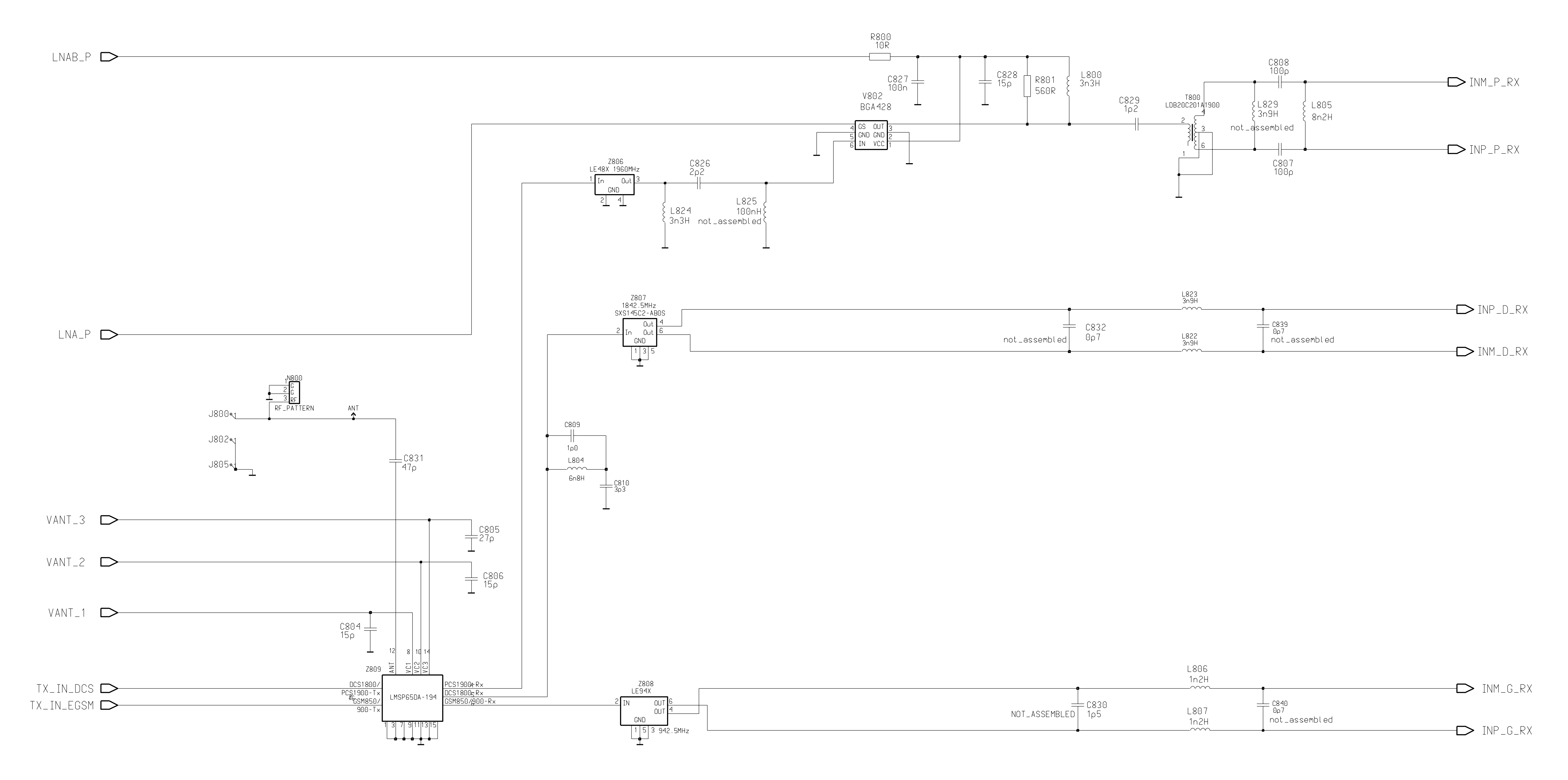
RH-12/RH-28 Top Level, v. 0, ed. 37



RF Top Level, v. 0.0, ed. 59

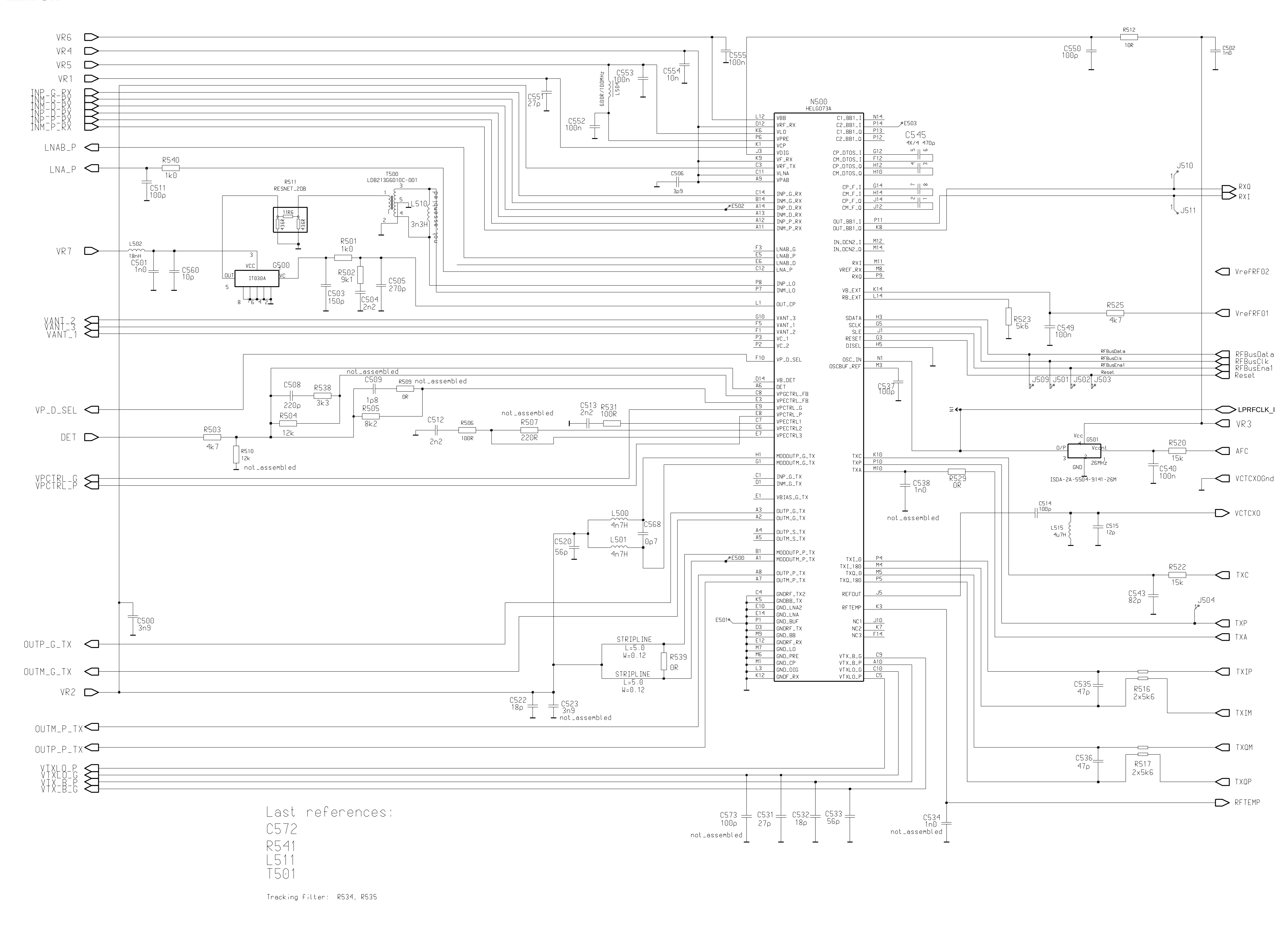


RX Front End and Antenna Switch, v. 0.0, ed. 75

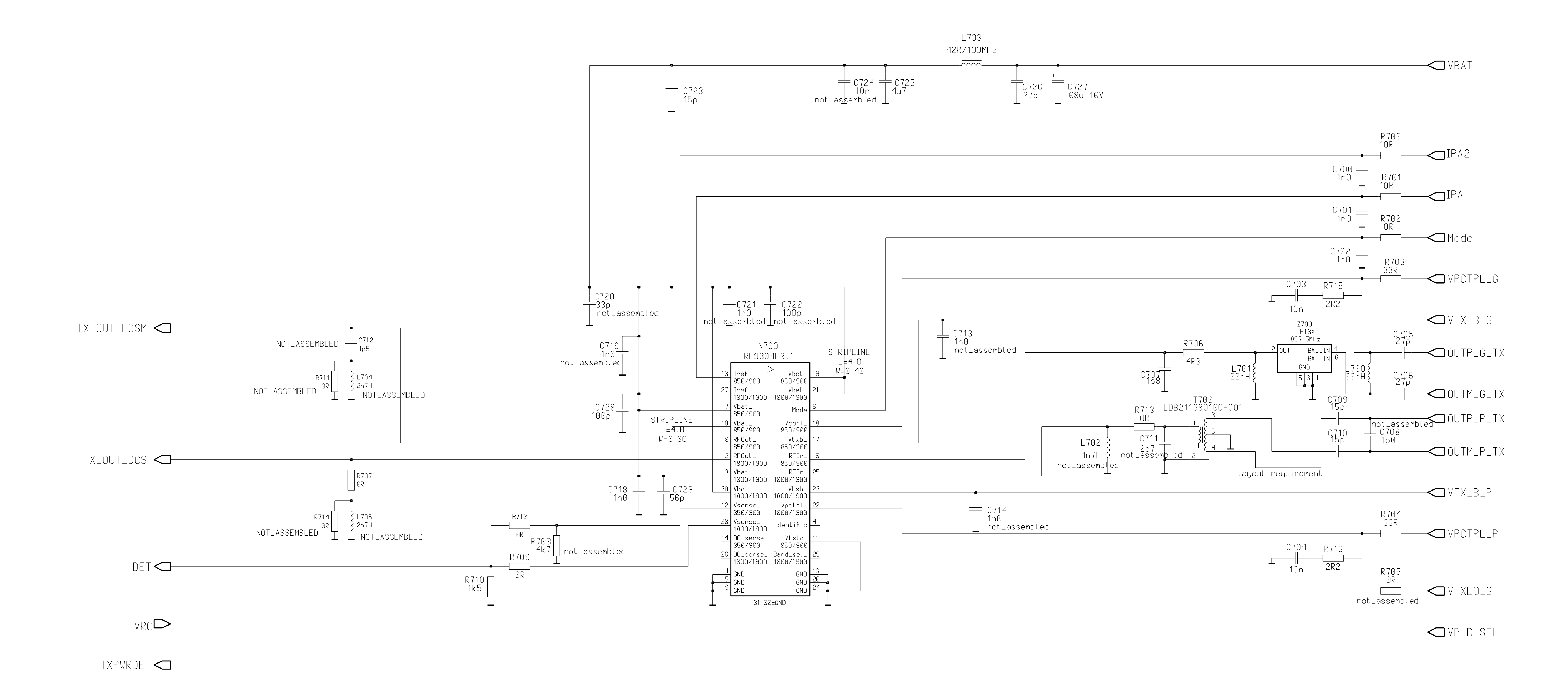


Last references:
C840
R807
L828
Z809
V800

HELGA

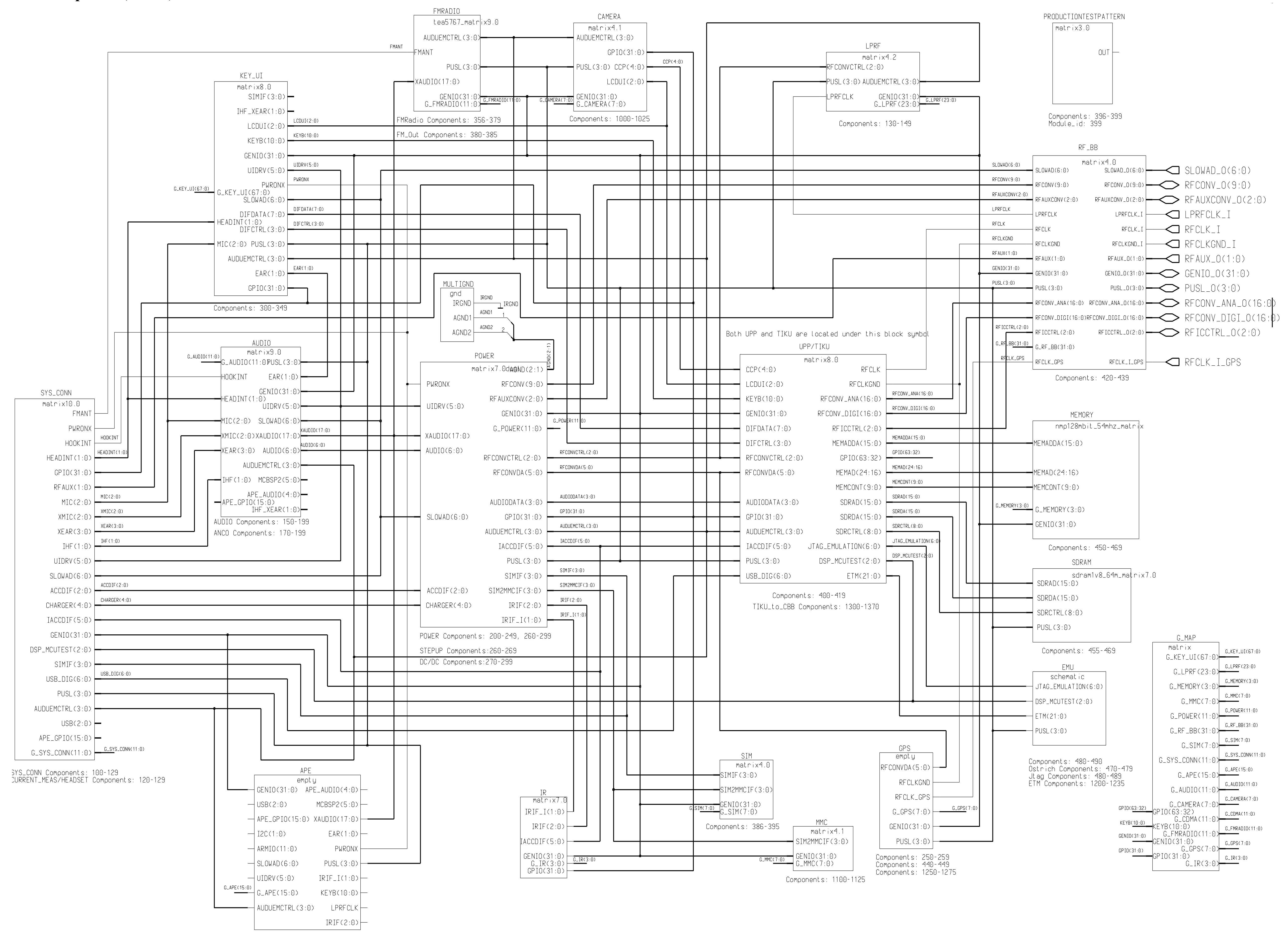

VR6
VR4
VR5
VR1
INP_G_RX
INM_G_RX
INP_D_RX
INM_D_RX
INP_P_RX
INM_P_RX
LNAB_P
LNA_P
VR7
VANT_2
VANT_3
VANT_1
VP_D_SEL
DET
VPCTRL_G
VPCTRL_P
OUTP_G_TX
OUTM_G_TX
VR2
OUTM_P_TX
OUTP_P_TX
VTXLO_P
VTXLO_G
VTX_B_P
VTX_B_G
N500
HELGO73A
RXQ
RXI
VrefRF02
VrefRF01
RFBusData
RFBusClk
RFBusEna1
Reset
LPRFCLK_I
VR3
AFC
VCTCXOGnd
VCTCXO
TXC
TXP
TXA
TXIP
TXIM
TXQM
TXQP
RFTEMP
STRIPLINE
L=5.0
W=0.12
not_assembled
Last references:
C572
R541
L511
T501
Tracking Filter: R534, R535

RF9204 Power Amplifier and Power Detection, v. 0.2, ed. 63



Last references:
C731
R714
L705

BR4.5 Top Level, v. 6.1, ed. 29

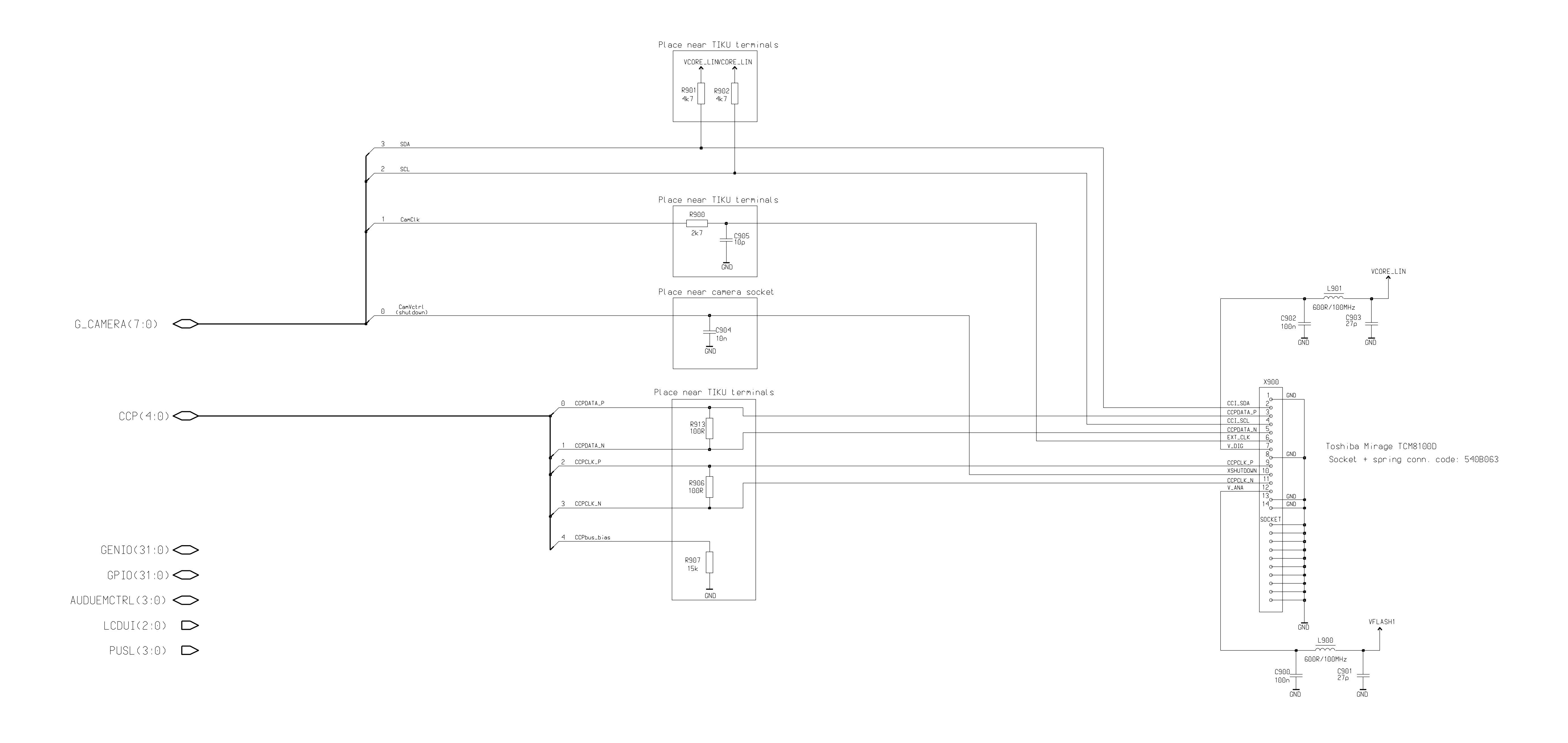


BR45 Audio, v. 1.6, ed. 167

IHF speaker connection

Place close to system connector

Mirage 2 VGA Camera, v. 1.1, Ed. 194



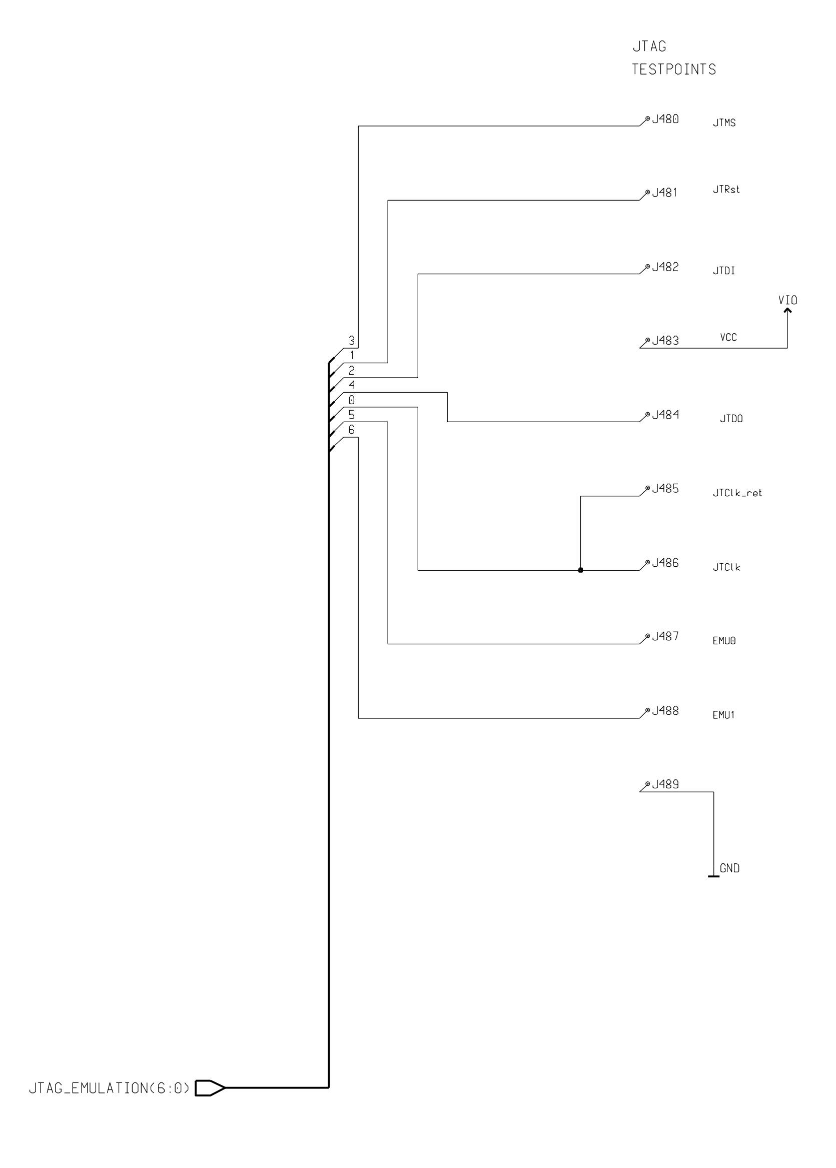
Test and Emulator Interface, v. 2.0, ed. 41



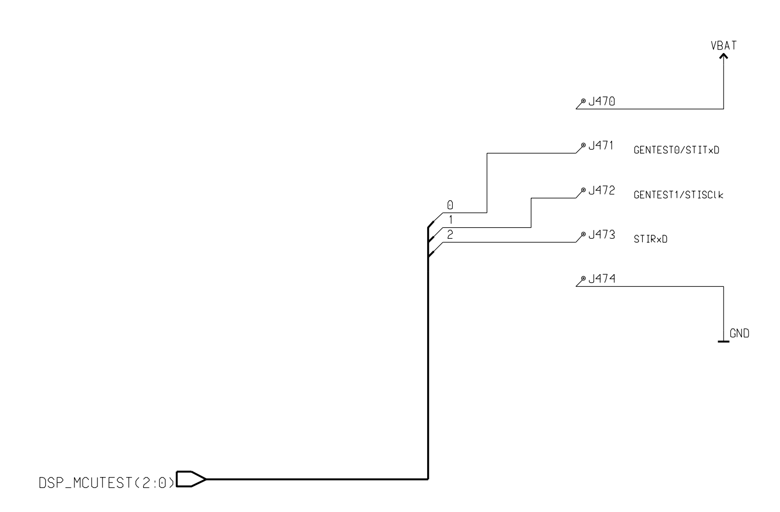




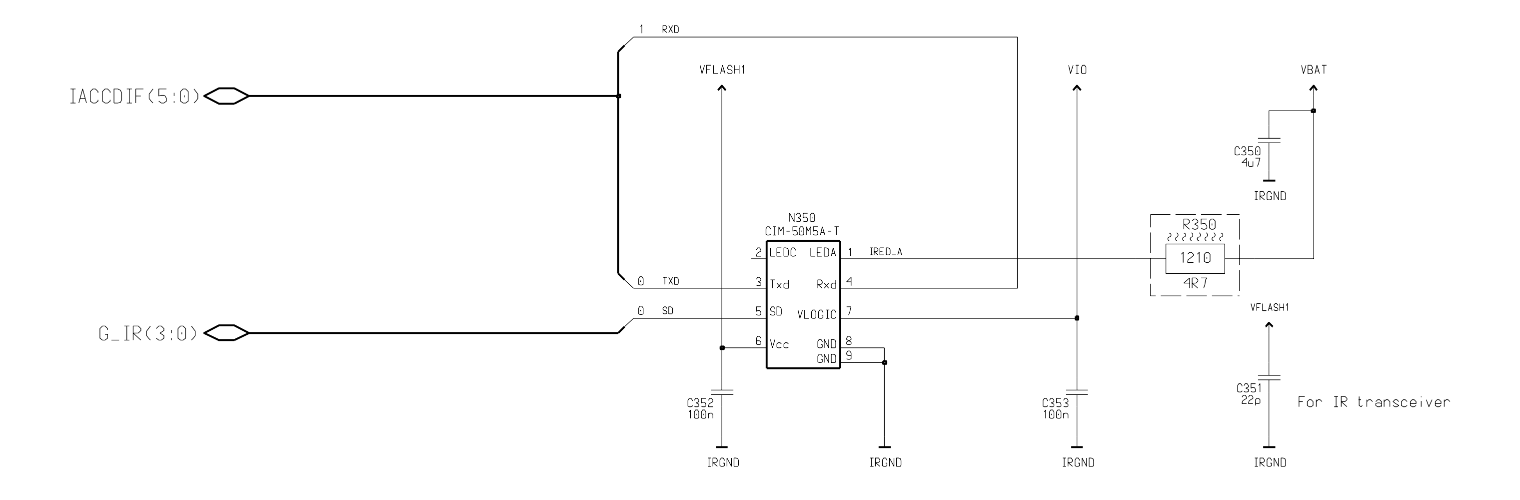
Testpoints for JTAG Emulator, v. 1.3, ed. 12



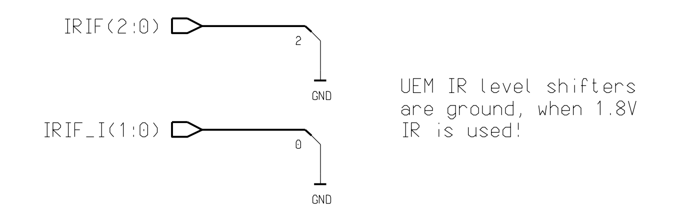
Testpoints Based Ostrich Interface, v.1.3, ed. 12



Citizen IR Module 1.8V, v. 6.1.8, ed. 55



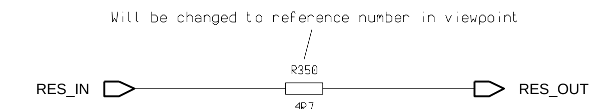
GENIO(31:0)
GPIO(31:0)



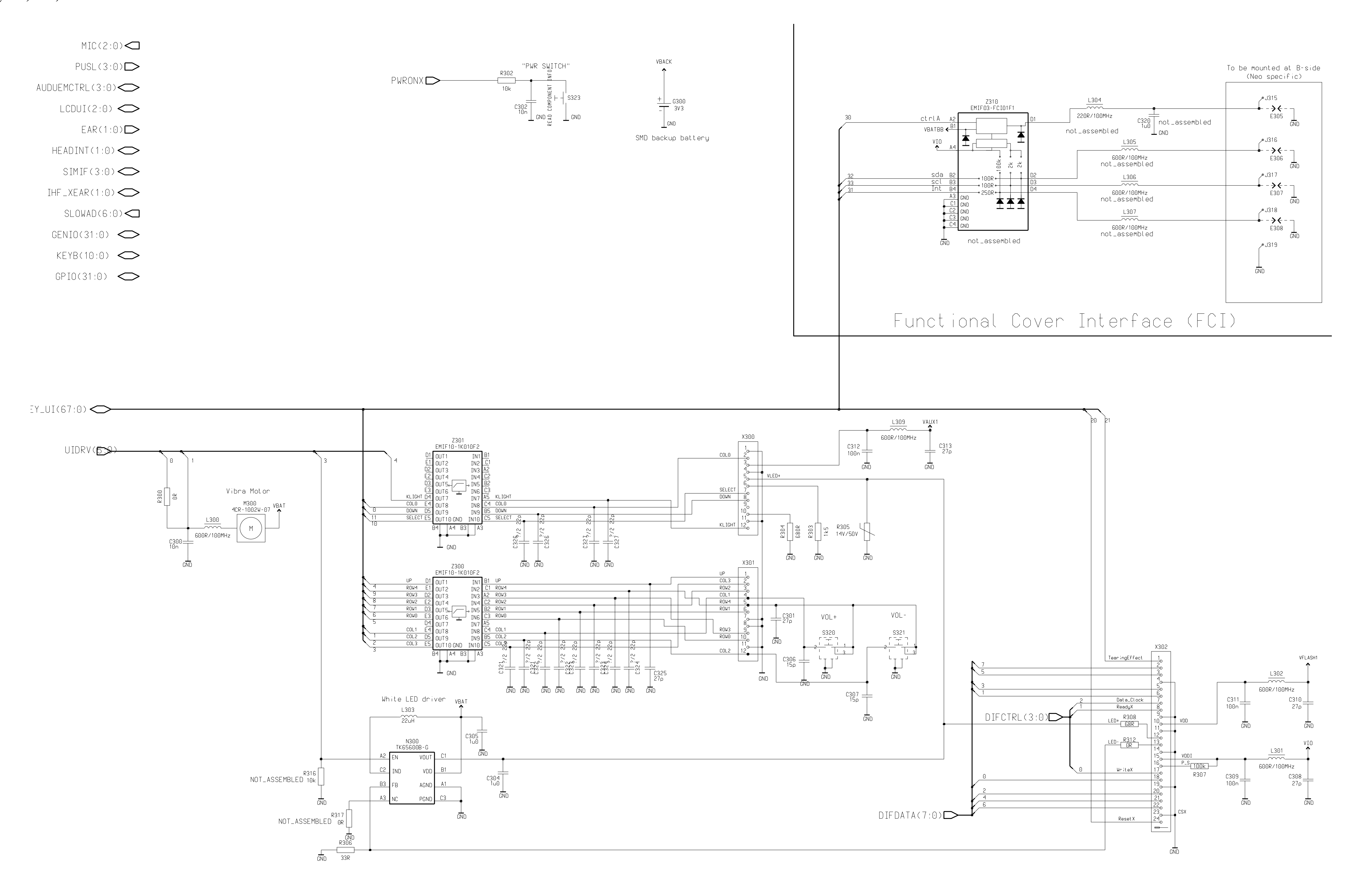
Used referenses

C 350 - 353
N 350
R 350

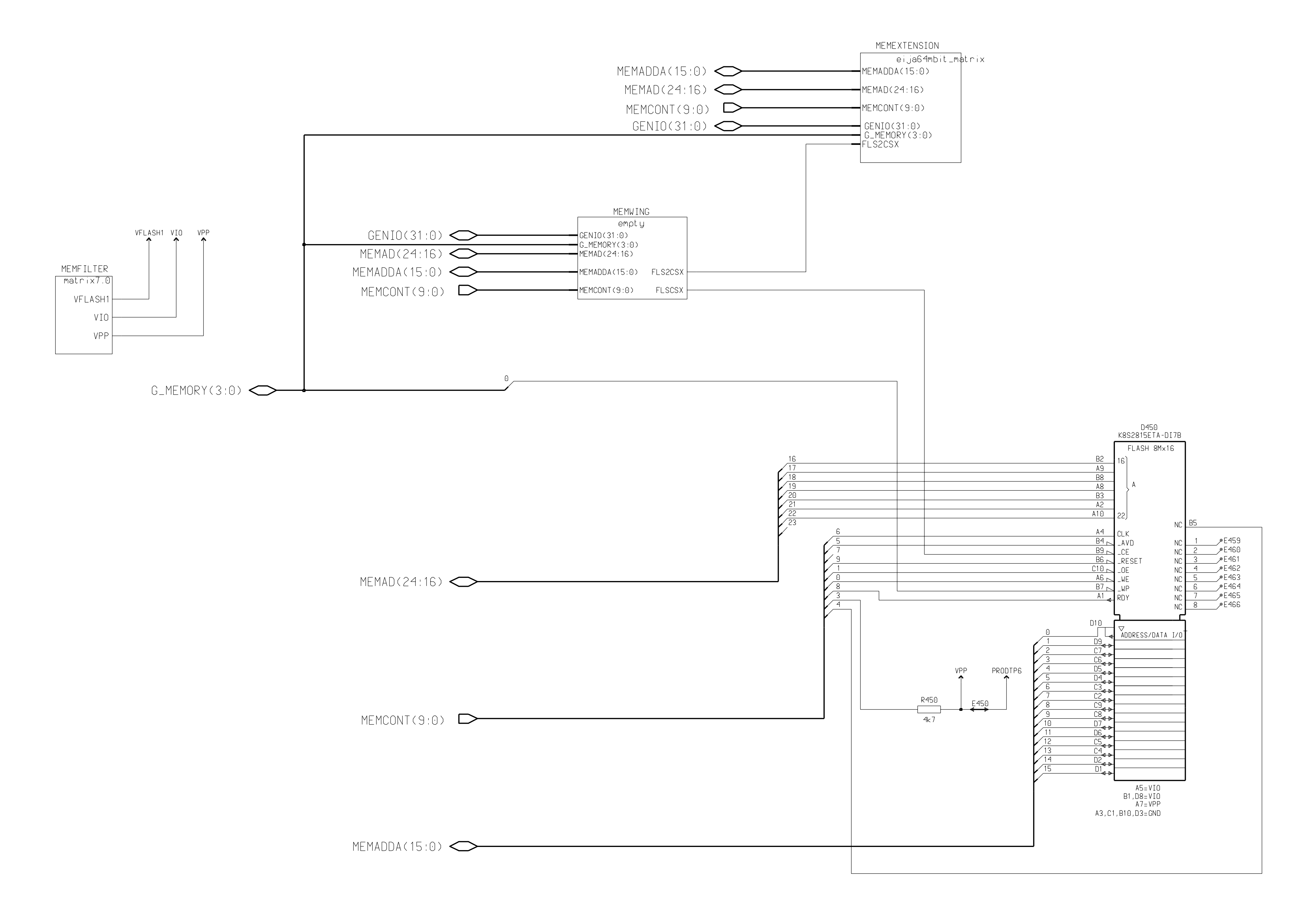
IR Resistor 1210, v. 0, ed. 6



Key UI, v. 1, ed. 326



Flash Memory 128 Mbit, v. 2.0, ed. 74



Discrete Capacitors for two Memories without VFlash1, v. 1.3, ed.11

Decoupling capacitors for 1st flash



Decoupling capacitors for 2nd flash



VFLASH1

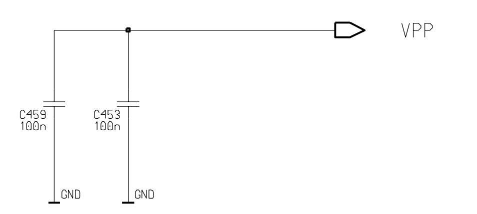
VPP

C454
100n

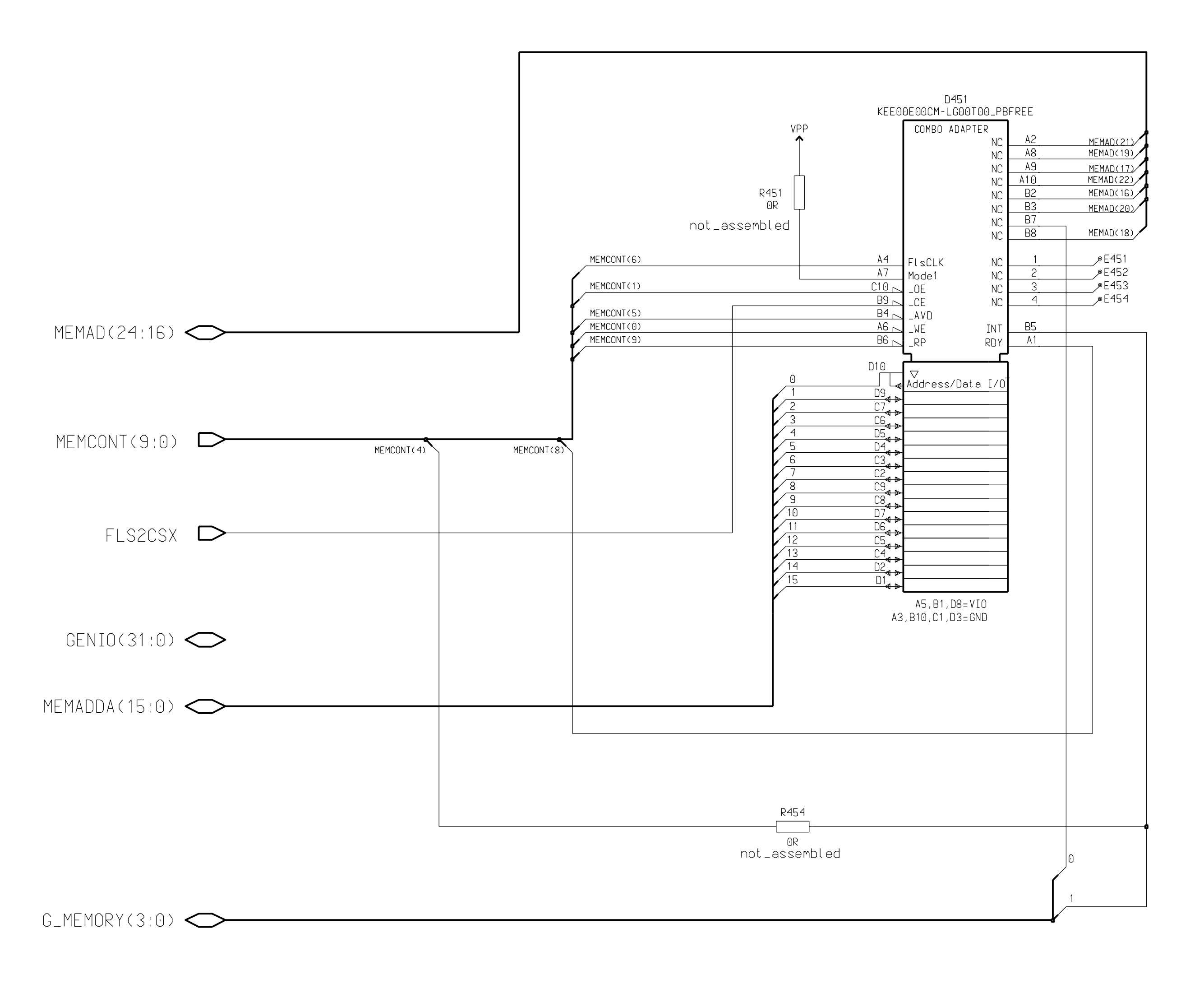
C452
100n

GND

GND



Flash Memory 64 Mbit NAND, v. 2.0, ed. 62



Resistors R451 and R454 are added into schematics to make it possible to replace NAND FLASH by NOR FLASH if necessary. Later when NAND FLASH will achieve more mature state, these resistors will be removed.

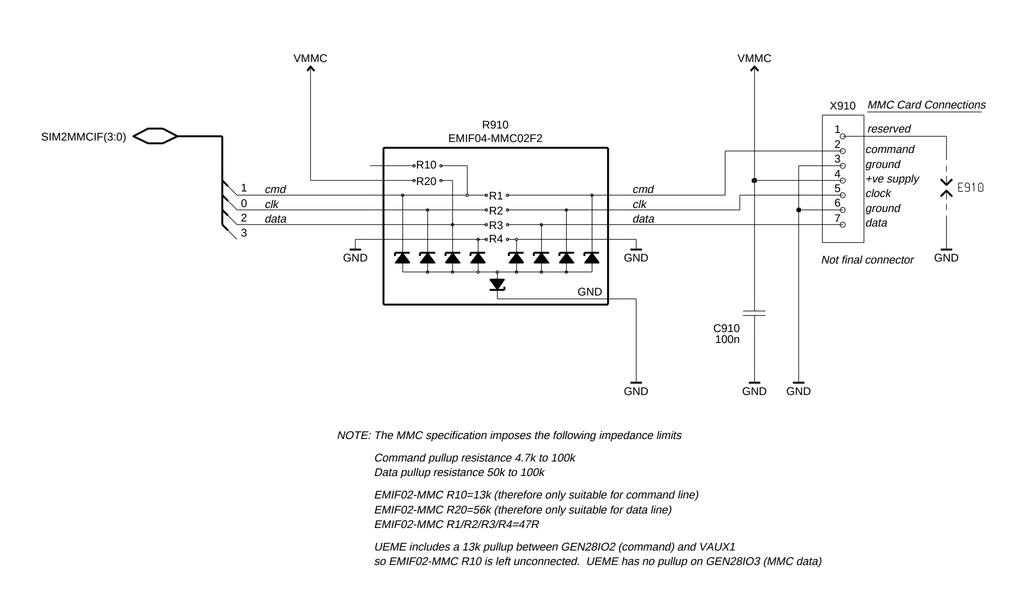
In case of NAND FLASH, memory pin A7 and B7 need to be n.c. on PWB. B5 need to be connected to GENIO(13) only.

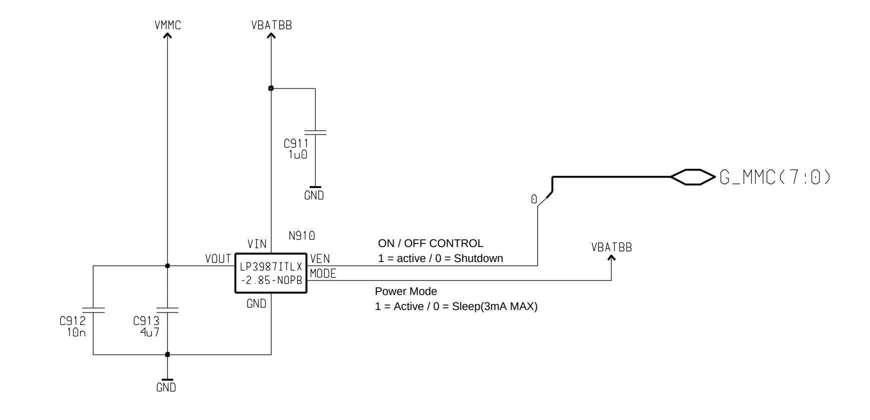
Empty Wing Sheet, v. 0, ed. 10



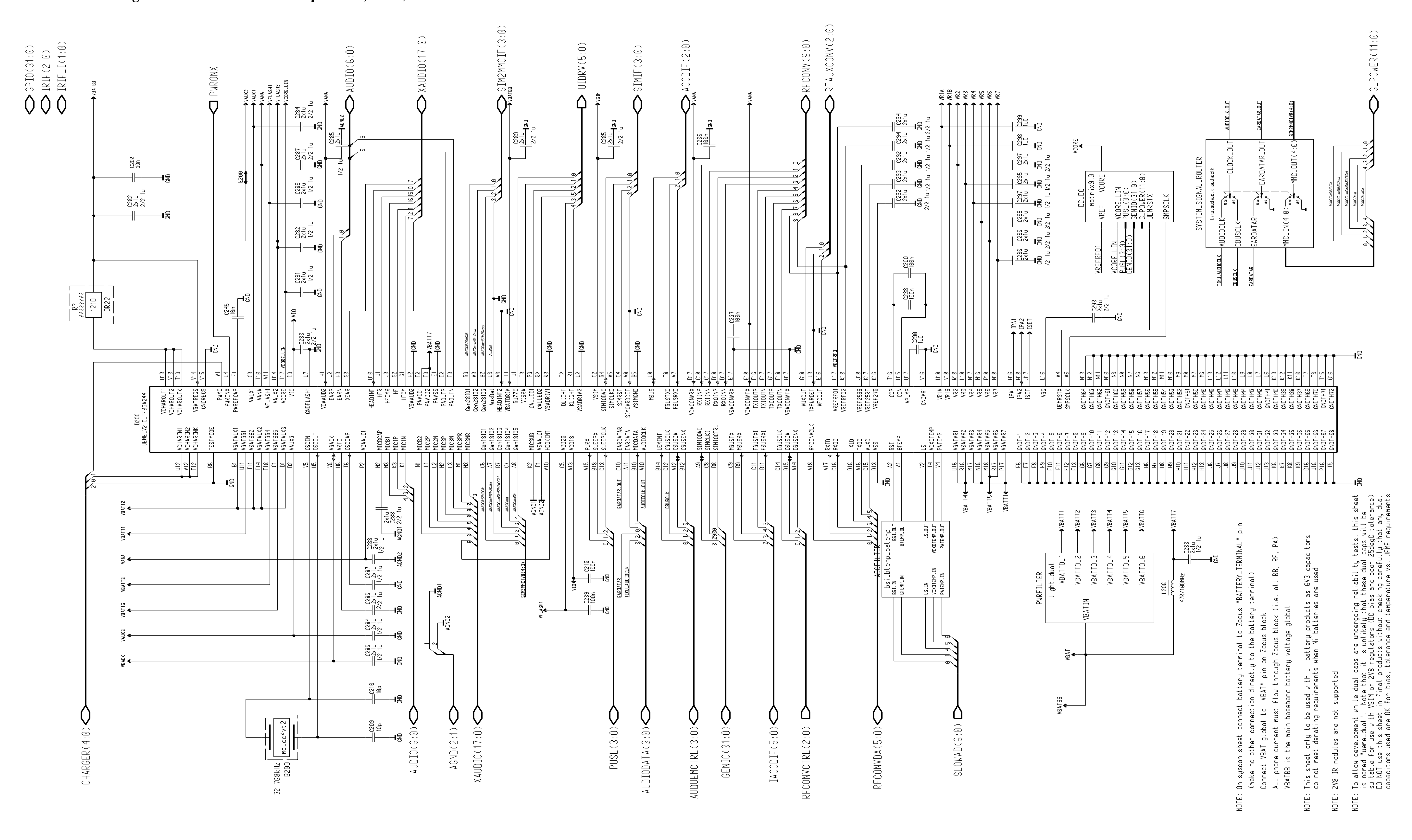
MMC Interface, v. 2.1, ed. 79

GENIO(31:0)

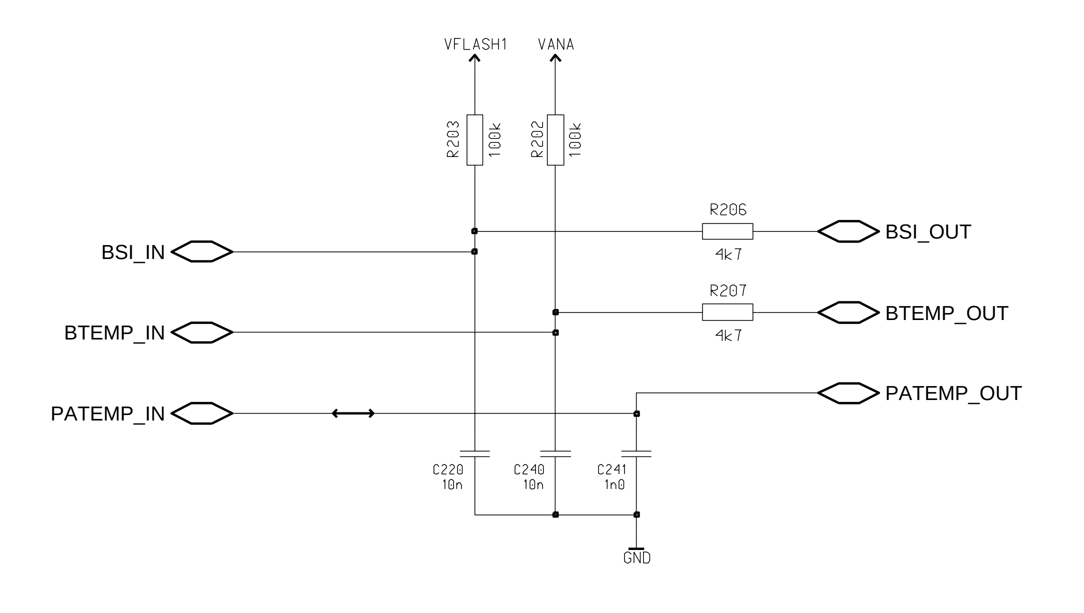




UEME Power Management - Dual 1 uF 6V3 Capacitors, v 1.0, ed. 307



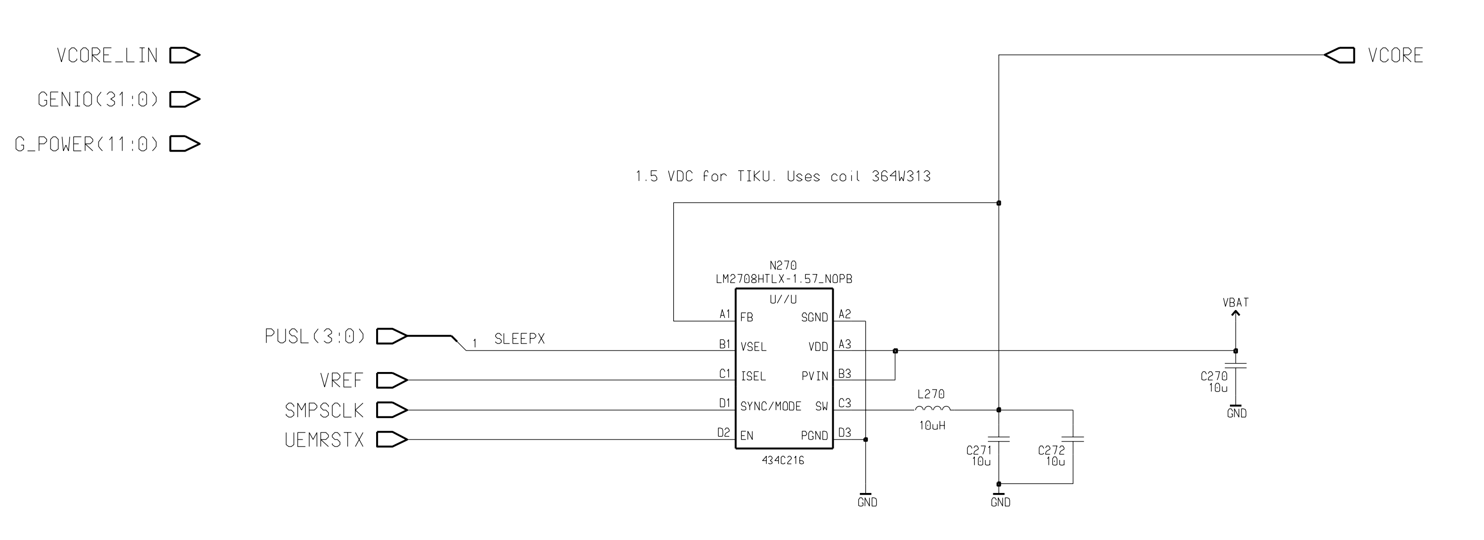
UEME ADC Filter Block - BSI, BTEMP and Active PATEMP, v. 1.0



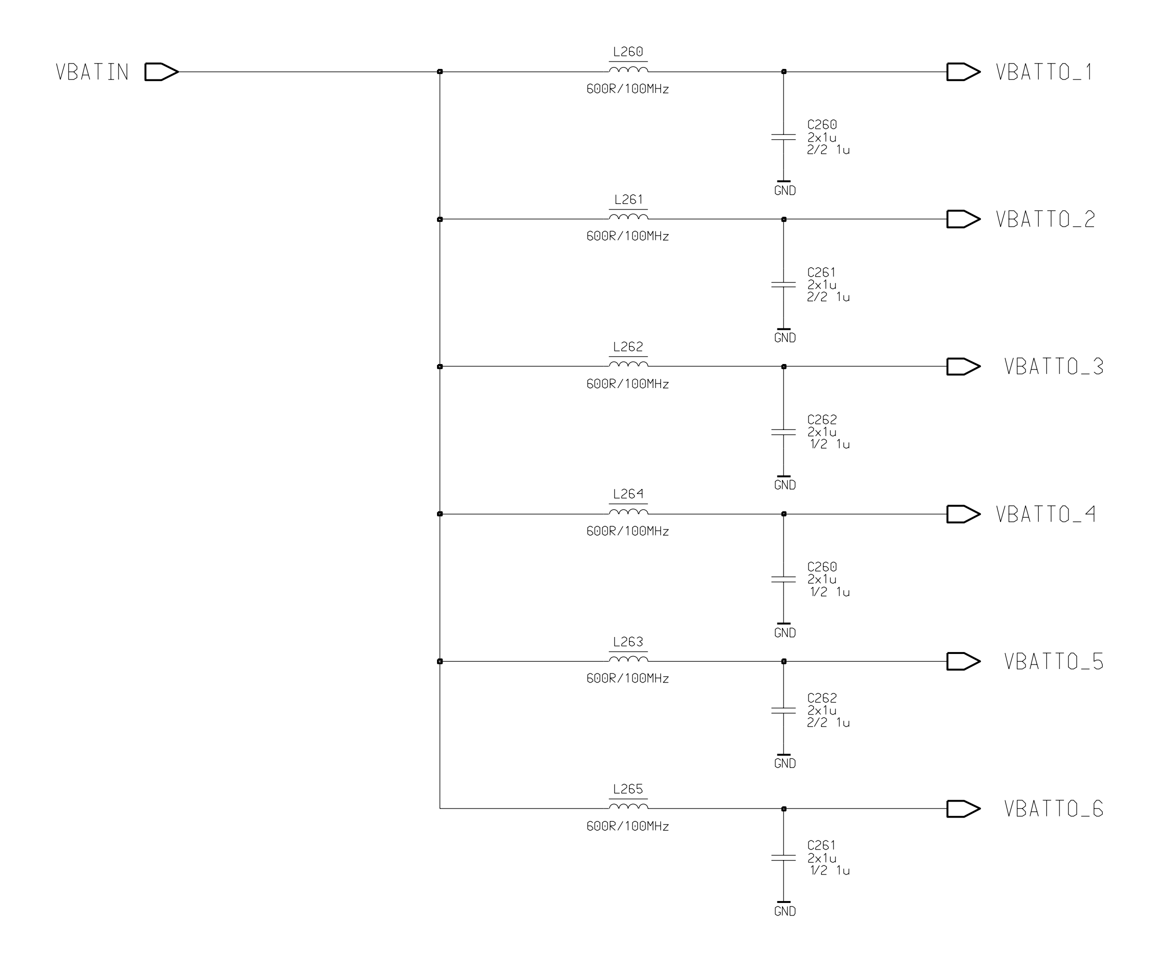
LS_IN
VCXOTEMP_IN
LS_OUT
VCXOTEMP_OUT
GND

NOTE: Helga RF drives PATEMP directly
so PATEMP does not need a pullup

DC_DC for TIKU and VIO, v. 1.7, ed. 75

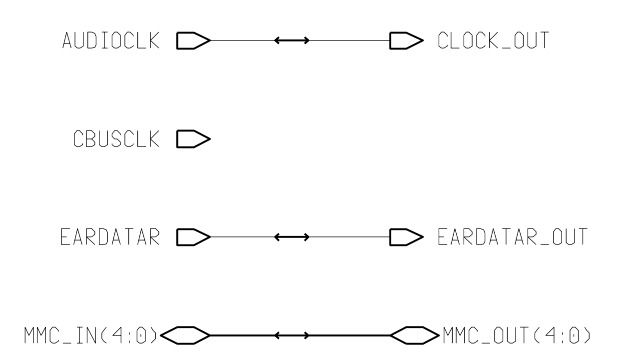


Light Filtering for Projects using 1uF Caps, v. 1.0



NOTE: This sheet uses dual 1uF capacitors. Check that full approval has been granted for these parts before use, or use this sheet at risk

Digital-ASIC-dependent Signal Routing - TikuXX Systems, v. 1.0



E916

32KHz Crystal - Micro Crystal CC4VT2, v. 0, ed. 6

Will be changed to reference number in viewpoint

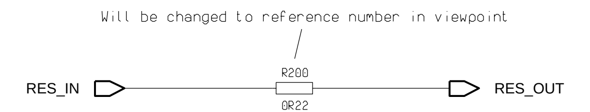
B200
32.768kHz

A

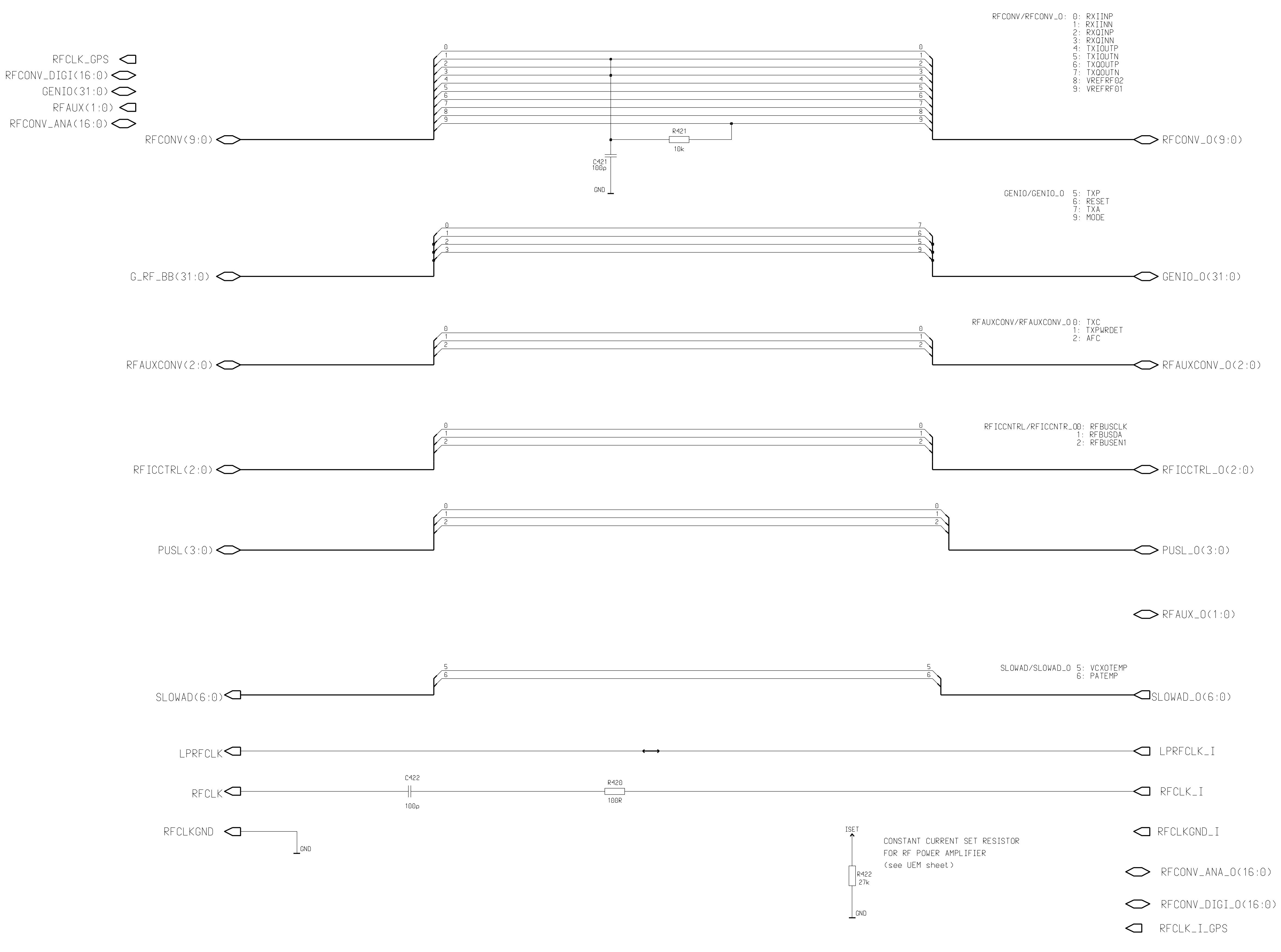
B

PlotDate: Dec 19 09:03:29

PWR Resistor 1210, v. 0, ed. 4

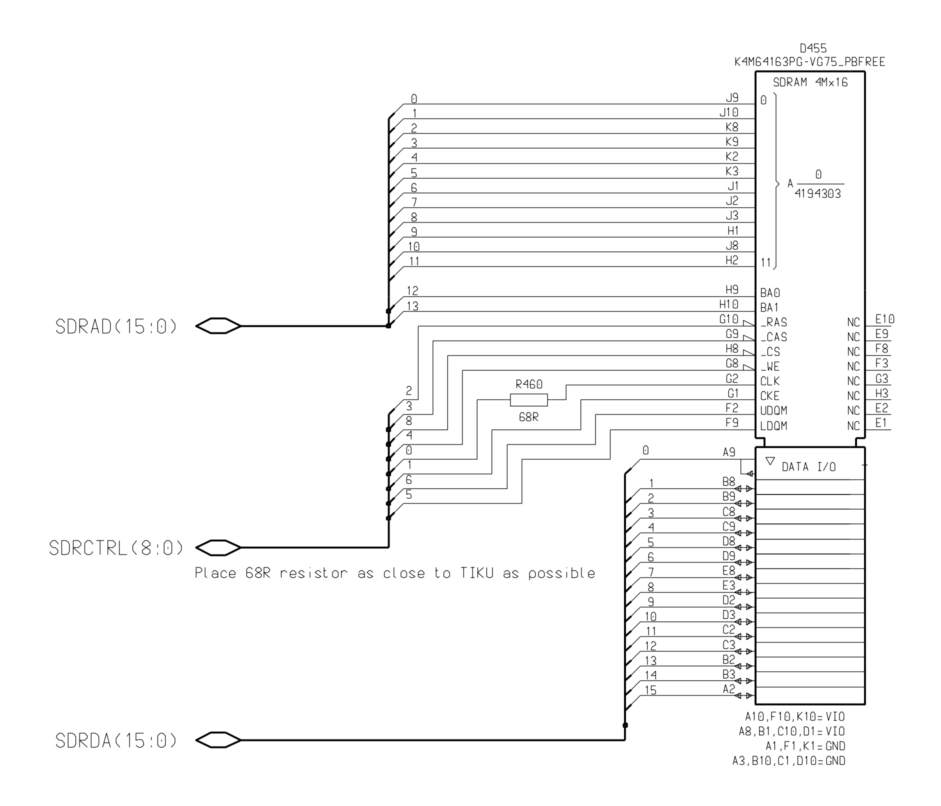


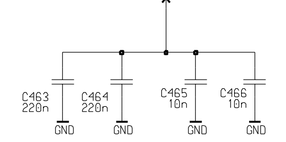
GSM RF - Baseband Interface, v. 1.3, ed. 64



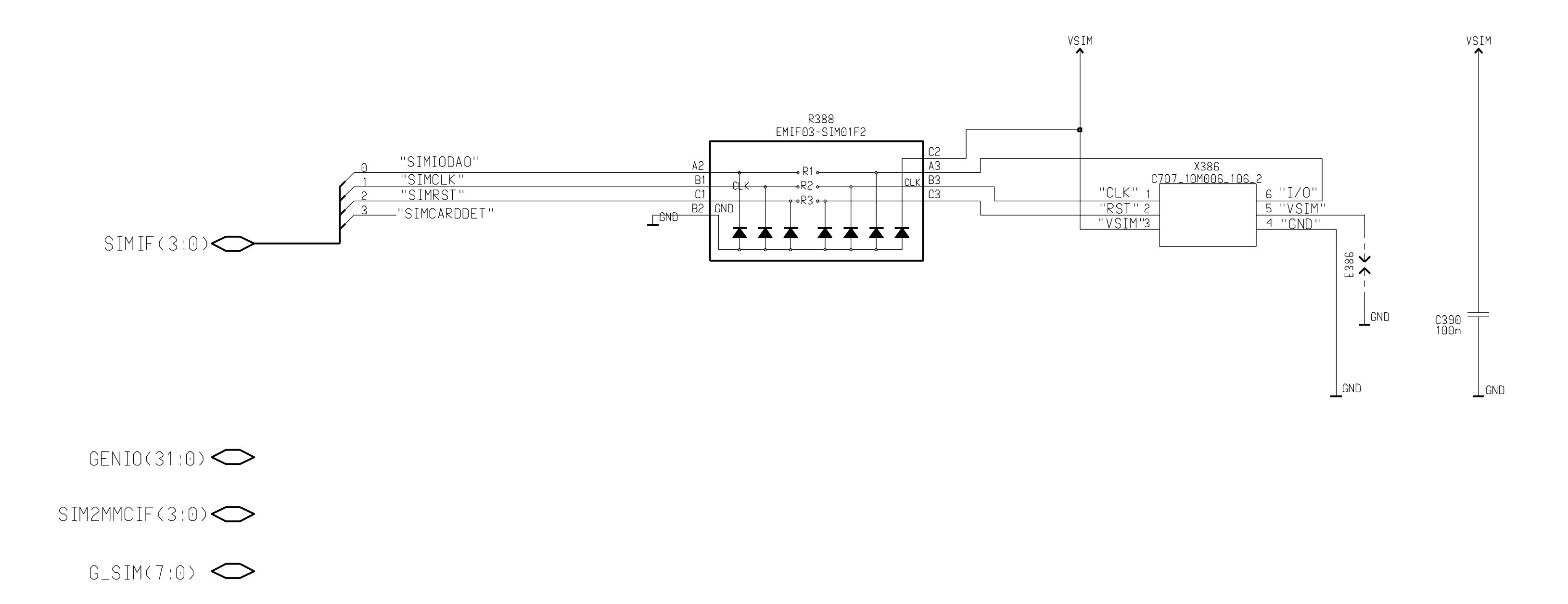
IPA1 AND IPA2 ARE USED IN RF, THE TOLERANCE OF R422 IS 1% (0402, 1430873)

64Mbit 1V8 SDRAM Memory, v. 1.0, ed. 64

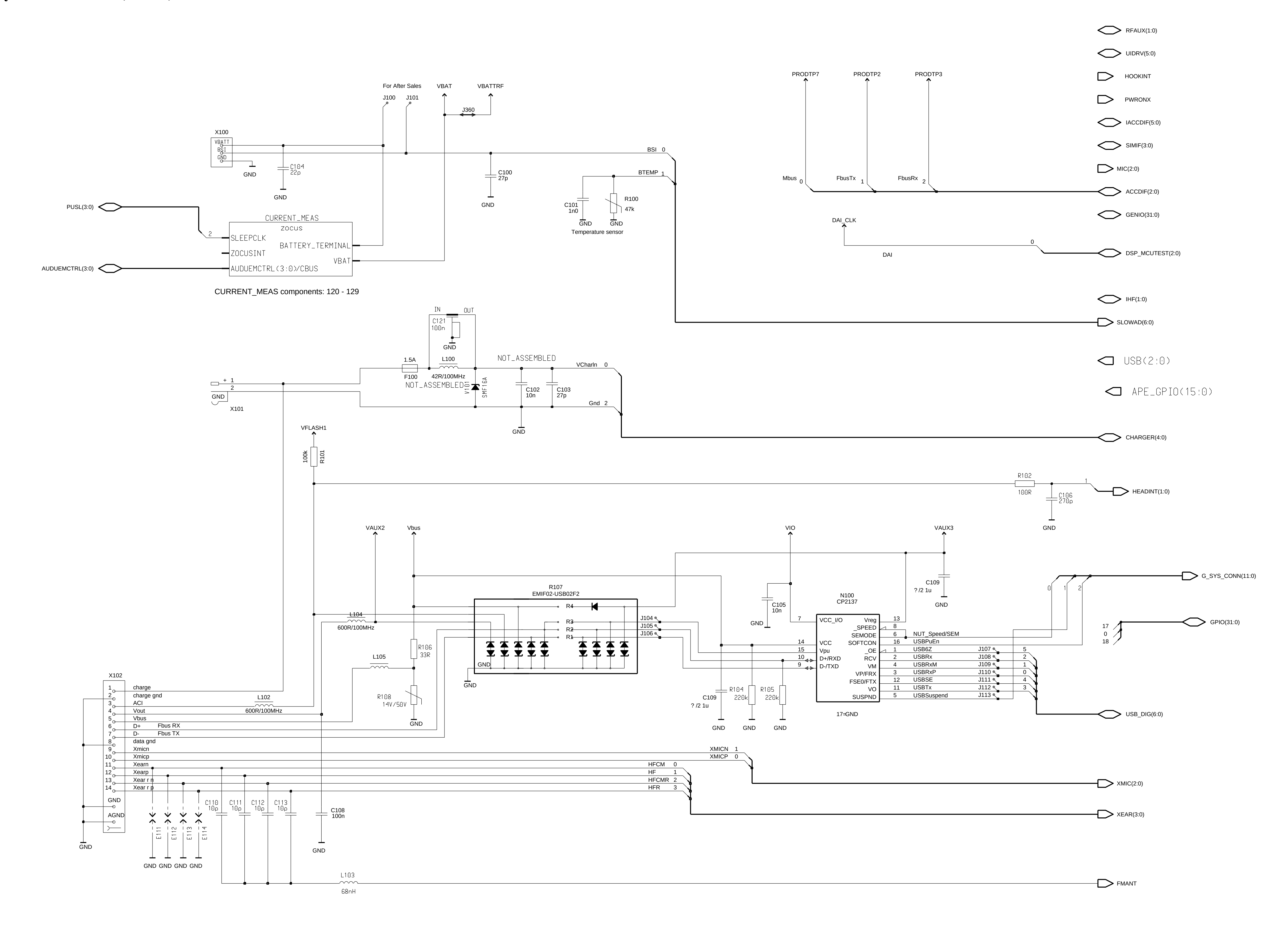




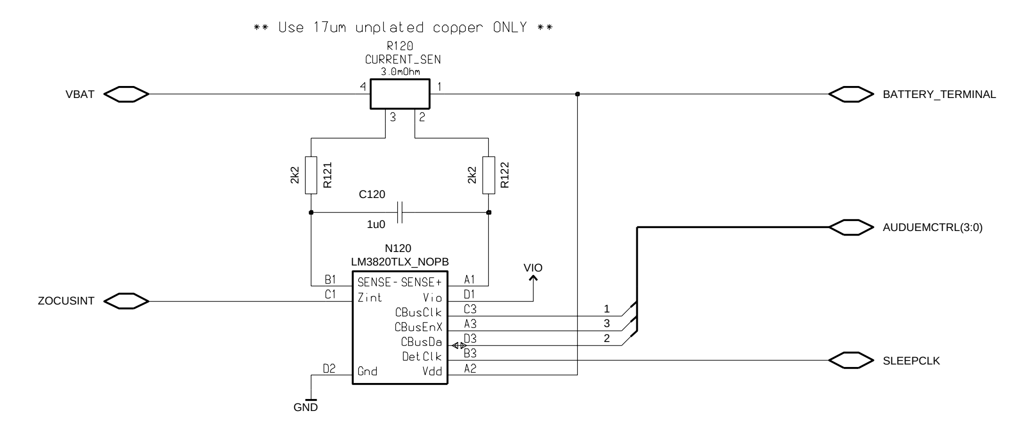
SIM Reader, v. 1.3, ed. 66



System Connector, v. 0.0, ed. 196

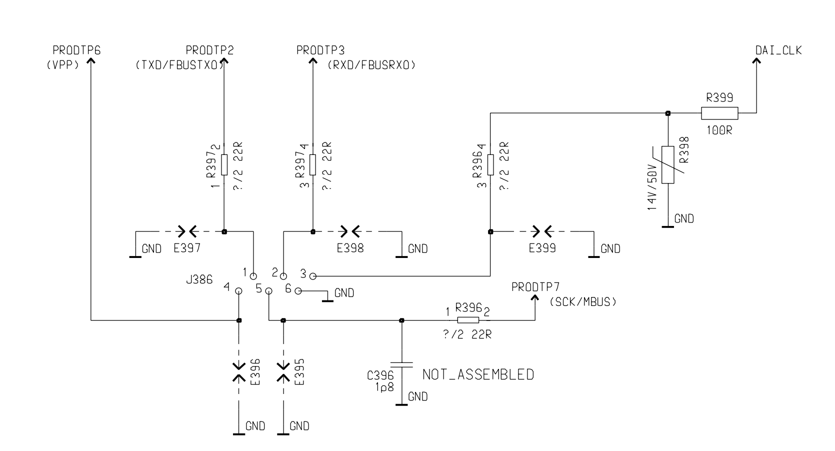


Current Measure, v. 0, ed. 18

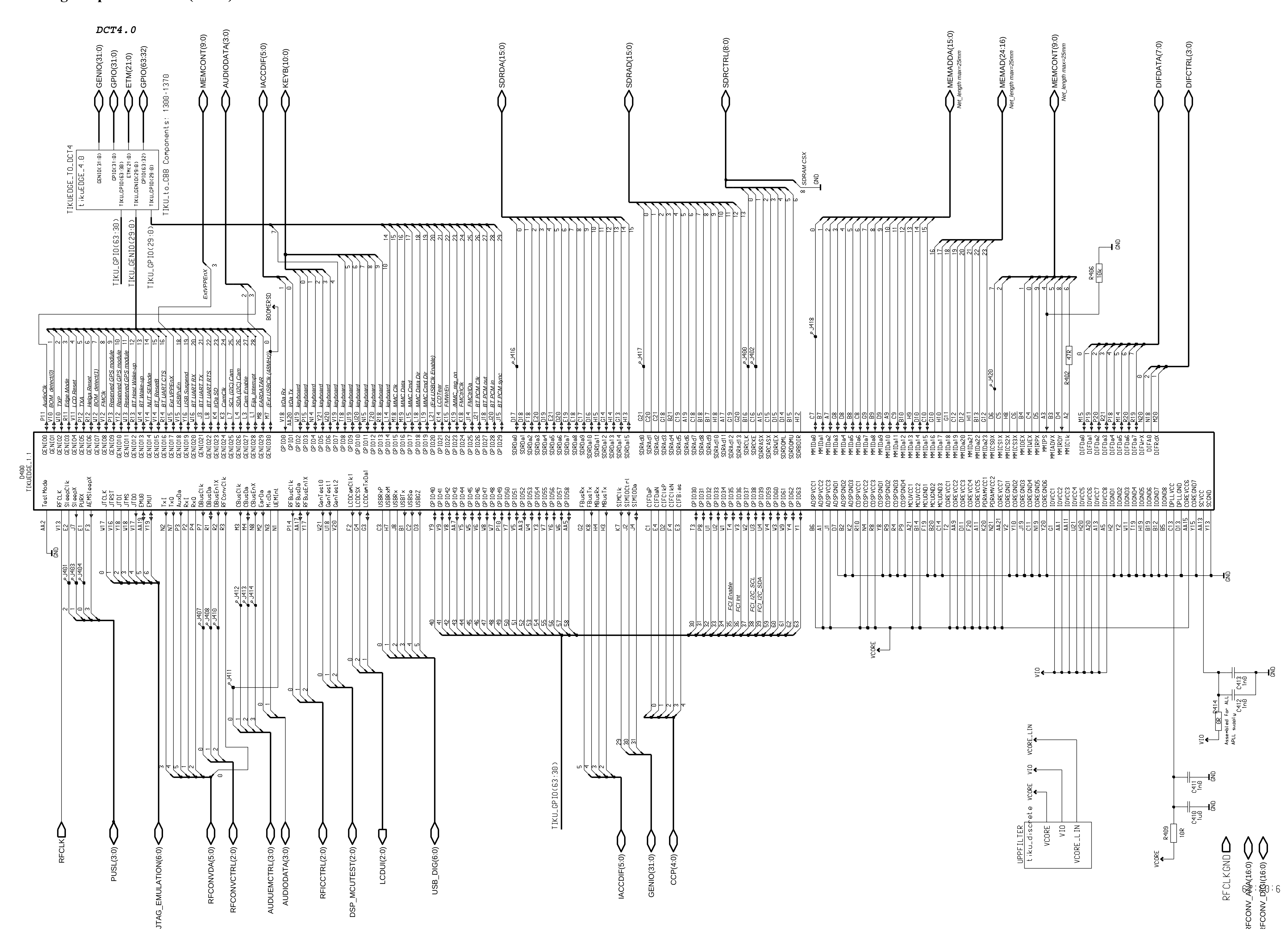


5 pin Production Test Pattern, v. 2.0, ed. 52

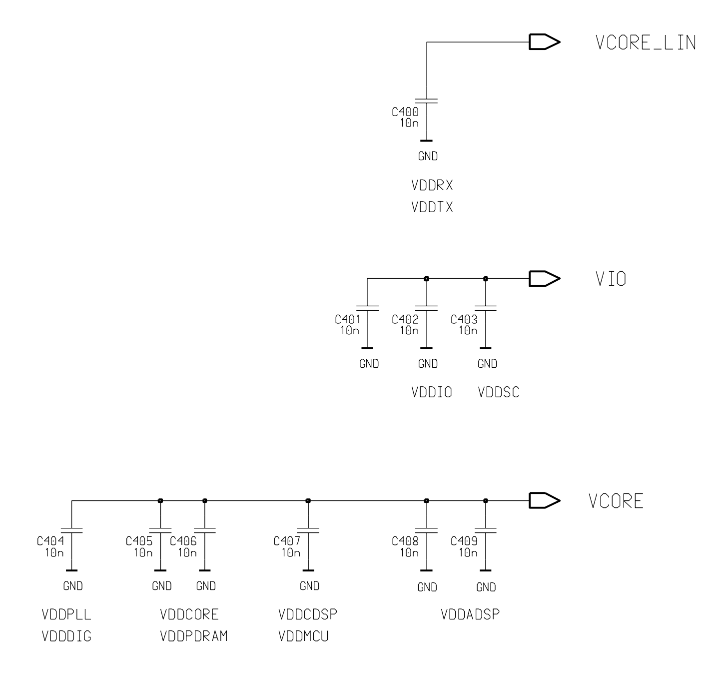
OUT



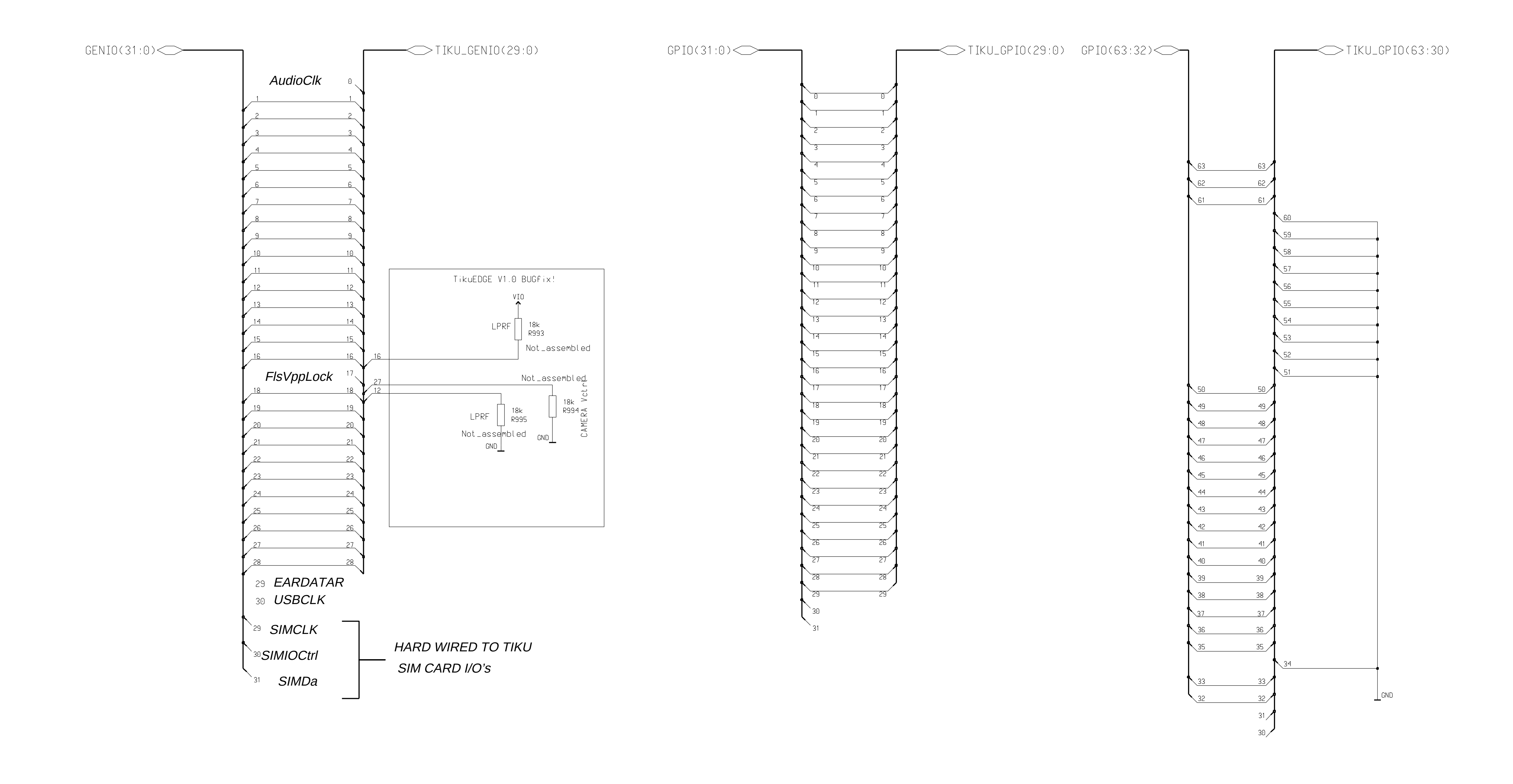
TIKUedge Implementation (GSM)



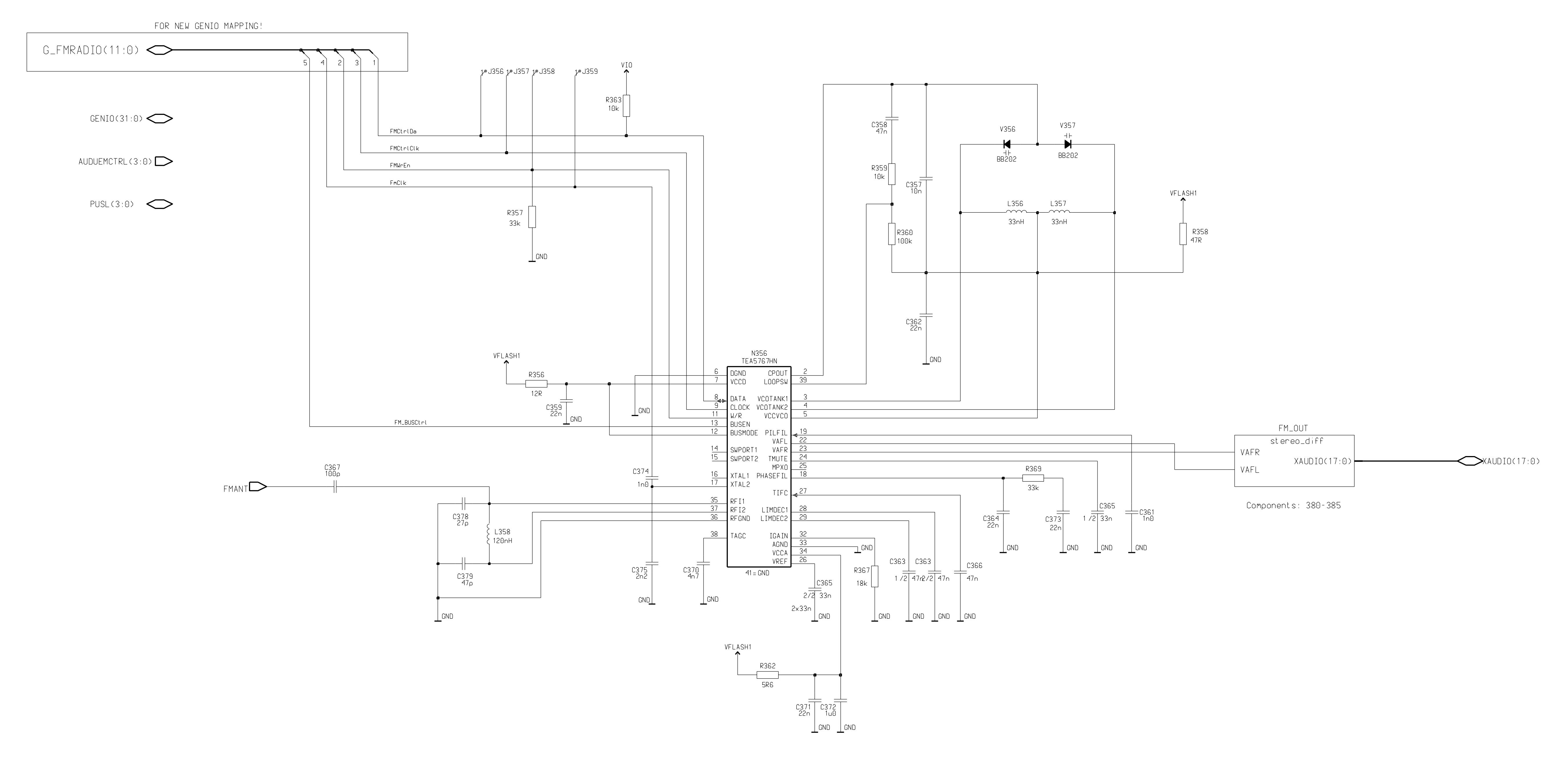
Discrete Decoupling Capacitors for Tiku, v. 0, ed. 12



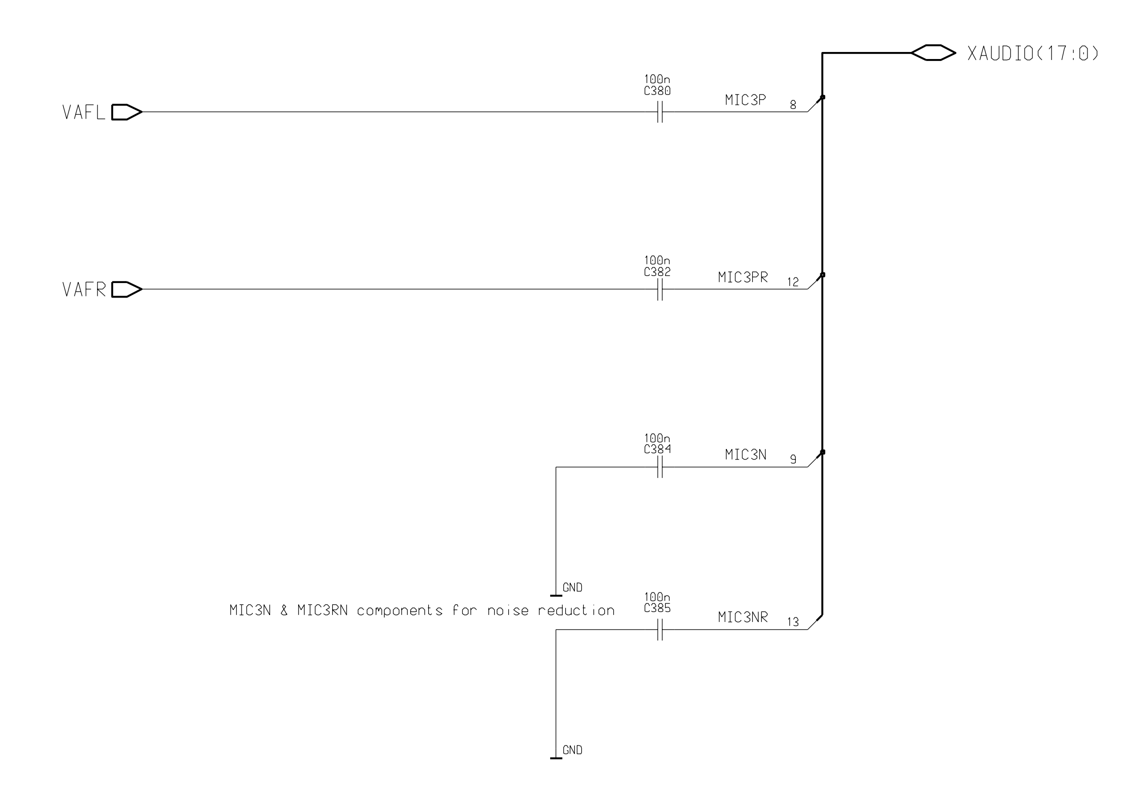
TikuEDGE Bus Conversion Sheet, v. 1.2, ed. 102



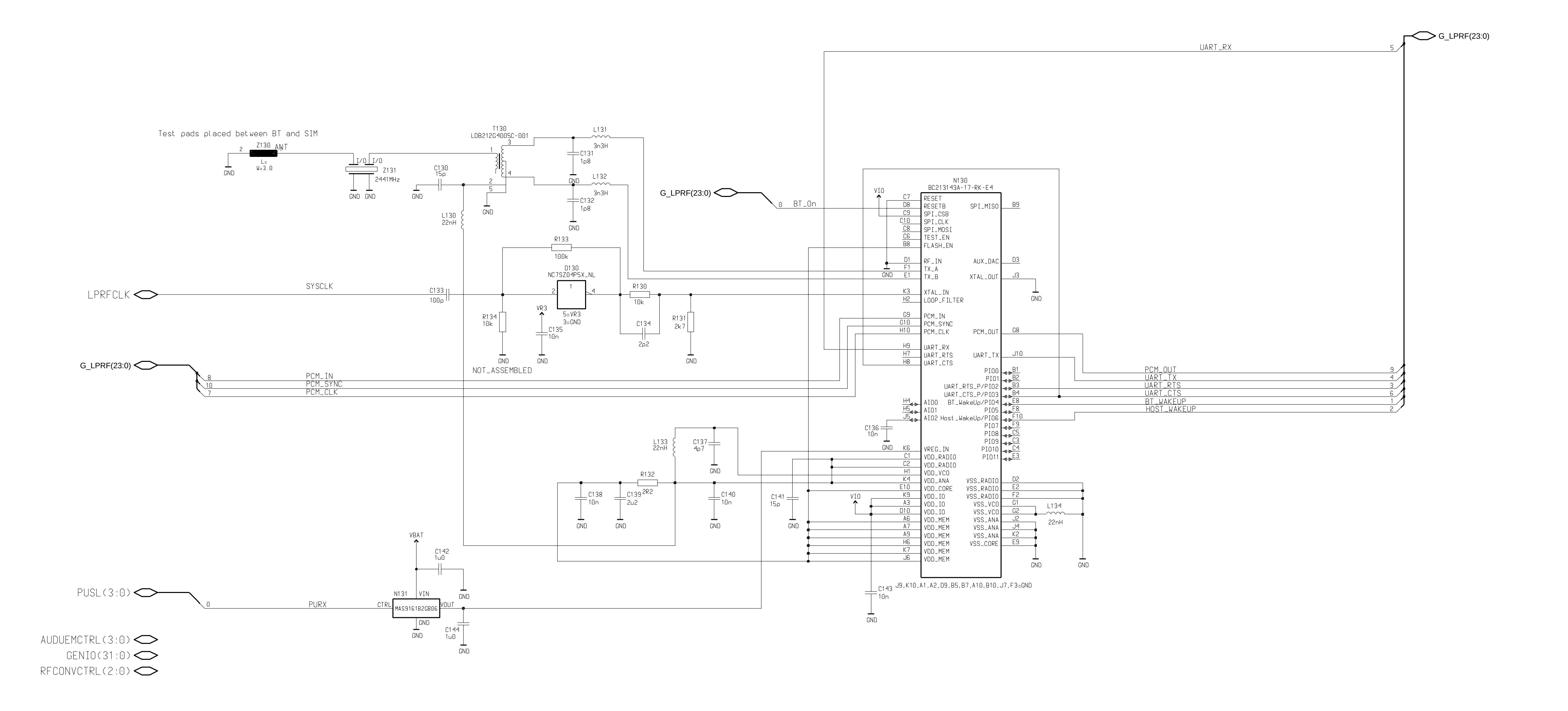
FM Radio Unit, v. 1.3, ed. 198



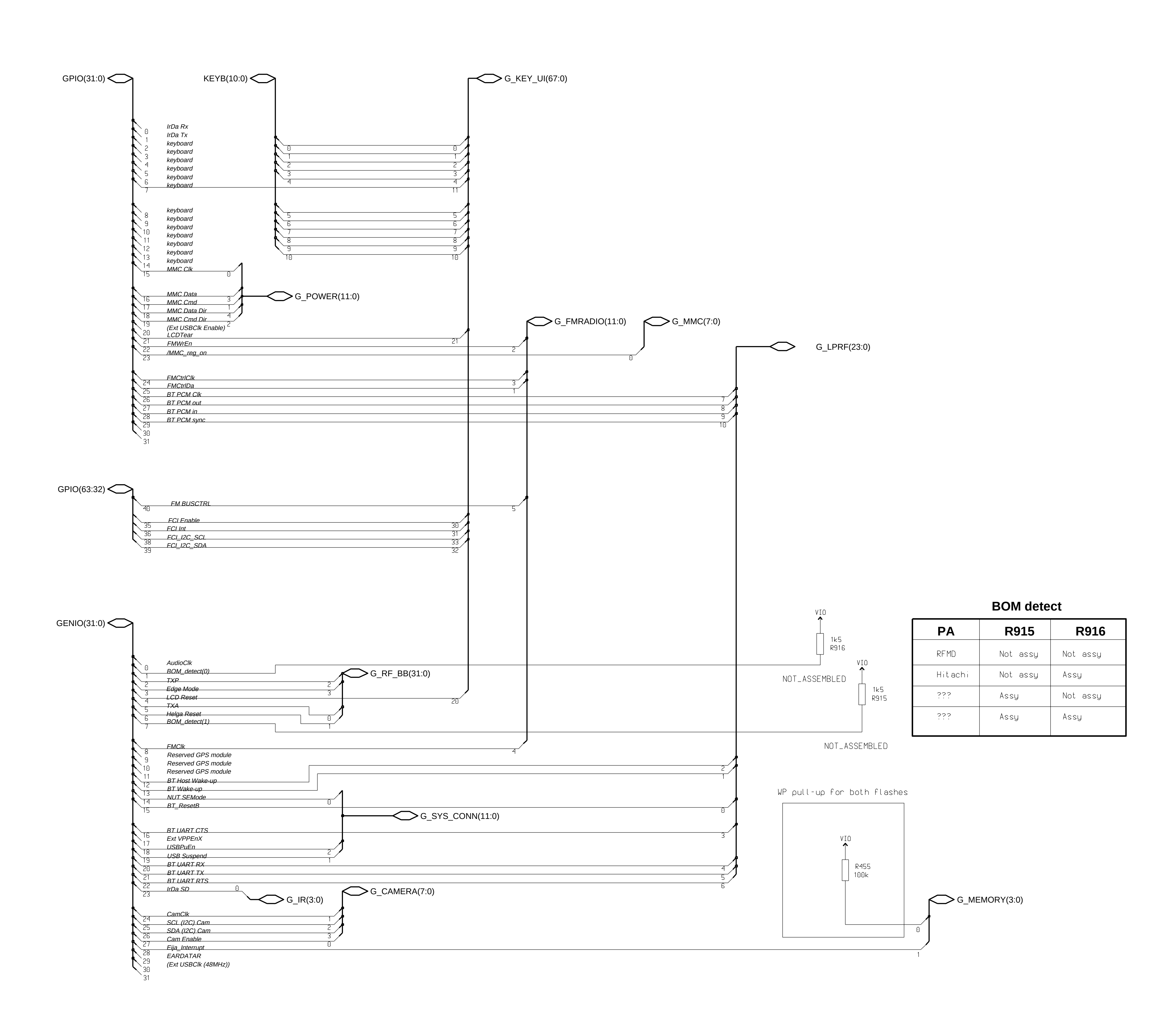
Differential Stereo. v. 0.0, ed. 7



Low Power RF Module



GENIO and GPIO Connection Block, v. 0.0, ed. 38



PA	R915	R916
RFMD	Not assy	Not assy
Hitachi	Not assy	Assy
???	Assy	Not assy
???	Assy	Assy